

**Amendments to the Specification**

Please **revise paragraph [45]** of the specification as follows:

**[45]** To describe particularly, after the main power is supplied to the power block ~~10~~ block 30, the gate low voltage VGL is supplied to the gate driver 26 from the power block 30 through the gate low voltage output line 32. At this moment, the capacitor C of the RC circuit 41 is charged with the voltage and the second N-type transistor Q2 remains at the turned-off state until this charged voltage combined with the voltage produced by a voltage drop from the first bias resistor RB1 becomes higher than a threshold voltage of the second N-type transistor Q2. Since the turned-off second transistor Q2 turns off the first transistor Q1, the gate high voltage VGH' is temporarily blocked by the turned-off state of the first P-type transistor Q1 so as not to be supplied to the gate driver 26.